

Abstracts

A 15 GHz Single Stage GaAs Dual-Gate FET Monolithic Analog Frequency Divider with Reduced Input Threshold Power (1988 [MCS])

K. Kanazawa, M. Kazumura and G. Kano. "A 15 GHz Single Stage GaAs Dual-Gate FET Monolithic Analog Frequency Divider with Reduced Input Threshold Power (1988 [MCS])." 1988 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 88.1 (1988 [MCS]): 47-50.

A 15 GHz single stage GaAs dual-gate FET monolithic analog frequency divider with the reduced input threshold power has been designed and fabricated. Use of the dual-gate structure for the FET mixer contributed to simplifying the circuit configuration. Introduction of the rejection filter at the output port resulted in reducing the input threshold power down to 1.4dBm.

 [Return to main document.](#)